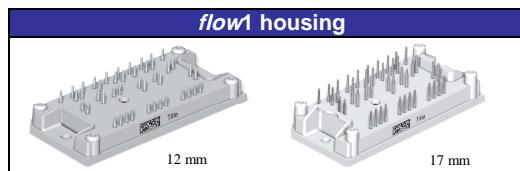
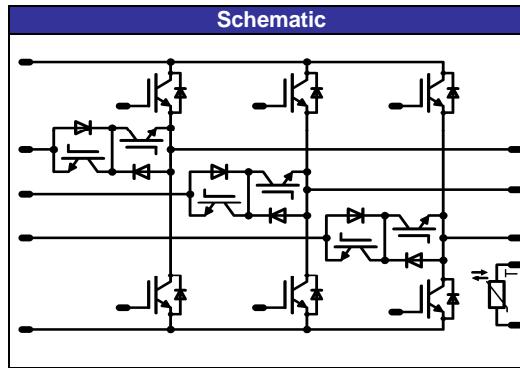


flow3xMNPC 1
1200 V/40 A

Features
<ul style="list-style-type: none"> • 3 phase mixed voltage component topology • neutral point clamped inverter • reactive power capability • low inductance layout



Target Applications
<ul style="list-style-type: none"> • solar inverter • UPS



Types
<ul style="list-style-type: none"> • 10-FY12M3A040SH-M749F08 • 10-F112M3A040SH-M749F09

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Half Bridge IGBT (T1,T4,T5,T8,T9,T12)

Collector-emitter break down voltage	V _{CE}		1200	V
DC collector current	I _C	T _j =T _j max T _h =80°C T _c =80°C	31 41	A
Pulsed collector current	I _{Cpulse}	t _p limited by T _j max	120	A
Power dissipation per IGBT	P _{tot}	T _j =T _j max T _h =80°C T _c =80°C	75 114	W
Turn off safe operating area	I _C	T _j ≤150°C V _{CE} ≤V _{CES}	120	A
Short circuit ratings	t _{sc} V _{CC}	T _j ≤150°C V _{GE} =15V	10 800	μs V
Gate-emitter peak voltage	V _{GE}		±20	A
Maximum Junction Temperature	T _j max		175	°C

Neutral P. FWD (D2,D3,D6,D7,D10,D11)

Peak Repetitive Reverse Voltage	V _{RRM}		600	V
DC forward current	I _F	T _j =T _j max T _h =80°C T _c =80°C	18 26	A
Surge forward current	I _{FSM}	t _p limited by T _j max	300	A
Power dissipation per Diode	P _{tot}	T _j =T _j max T _h =80°C T _c =80°C	30 45	W
Maximum Junction Temperature	T _j max		150	°C

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Neutral P. IGBT (T2,T3,T6,T7,T10,T11)				
Collector-emitter break down voltage	V _{CE}		600	V
DC collector current	I _C	T _j =T _{jmax} T _c =80°C	23 29	A
Pulsed collector current	I _{Cpuls}	t _p limited by T _{jmax}	90	A
Power dissipation per IGBT	P _{tot}	T _j =T _{jmax} T _c =80°C	37 56	W
Gate-emitter peak voltage	V _{GE}		±20	V
Short circuit ratings	t _{SC} V _{CC}	T _j ≤150°C V _{GE} =15V	6 360	μs V
Turn off safe operating area (RBSOA)	I _{offmax}	V _{CE} max = 600V T _{vj max} = 150°C	90	A
Maximum Junction Temperature	T _{jmax}		175	°C

Half Bridge FWD (D1,D4,D5,D8,D9,D12)

Peak Repetitive Reverse Voltage	V _{RRM}		1200	V
DC forward current	I _F	T _j =T _{jmax} T _c =80°C	12 14	A
Surge forward current	I _{FSM}	10 ms, sin 180° T _j = 150 °C	65	A
Power dissipation per Diode	P _{tot}	T _j =T _{jmax} T _c =80°C	28 43	W
Maximum Junction Temperature	T _{jmax}		175	°C

Thermal Properties

Storage temperature	T _{stg}		-40...+125	°C
Operation temperature under switching condition	T _{op}		-40...+(T _{jmax} - 25)	°C

Insulation Properties

Insulation voltage	V _{is}	t=2s	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm

Characteristic Values

Parameter	Symbol	Conditions				Value			Unit
		V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_f [A] or I_b [A]	T_j	Min	Typ	Max	

Half Bridge IGBT (T1,T4,T5,T8,T9,T12)

Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$		0,0015	$T_j=25^\circ C$ $T_j=125^\circ C$	5,2	5,8	6,4	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15	40	$T_j=25^\circ C$ $T_j=125^\circ C$	1,7	1,96 2,29	2,4	V
Collector-emitter cut-off current incl. Diode	I_{CES}		0	1200	$T_j=25^\circ C$ $T_j=125^\circ C$			0,005	mA
Gate-emitter leakage current	I_{GES}		20	0	$T_j=25^\circ C$ $T_j=125^\circ C$			120	nA
Integrated Gate resistor	R_{gint}						none		Ω
Turn-on delay time	$t_{d(on)}$	$R_{goff}=8 \Omega$ $R_{gon}=8 \Omega$	± 15	350	28	$T_j=25^\circ C$ $T_j=125^\circ C$	70 72		ns
Rise time	t_r					$T_j=25^\circ C$ $T_j=125^\circ C$	13 15		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ C$ $T_j=125^\circ C$	166 217		
Fall time	t_f					$T_j=25^\circ C$ $T_j=125^\circ C$	45 79		
Turn-on energy loss per pulse	E_{on}					$T_j=25^\circ C$ $T_j=125^\circ C$	0,31 0,52		mWs
Turn-off energy loss per pulse	E_{off}					$T_j=25^\circ C$ $T_j=125^\circ C$	0,67 1,16		
Input capacitance	C_{ies}	$f=1MHz$	0	25	$T_j=25^\circ C$		2300		pF
Output capacitance	C_{oss}						150		
Reverse transfer capacitance	C_{rss}						135		
Gate charge	Q_{Gate}		± 15	960	40	$T_j=25^\circ C$	185		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um $\lambda = 1 W/mK$					1,27		K/W

Neutral P. FWD (D2,D3,D6,D7,D10,D11)

Diode forward voltage	V_F			30	$T_j=25^\circ C$ $T_j=125^\circ C$		2,28 1,74	2,5	V
Reverse leakage current	I_r			600	$T_j=25^\circ C$ $T_j=125^\circ C$			100 500	μA
Peak reverse recovery current	I_{RRM}	$R_{goff}=8 \Omega$	± 15	350	28	$T_j=25^\circ C$ $T_j=125^\circ C$	32 41		A
Reverse recovery time	t_{rr}					$T_j=25^\circ C$ $T_j=125^\circ C$	18 40		ns
Reverse recovered charge	Q_{rr}					$T_j=25^\circ C$ $T_j=125^\circ C$	0,32 0,92		μC
Peak rate of fall of recovery current	$dI(rec)max/dt$					$T_j=25^\circ C$ $T_j=125^\circ C$	8818 3866		$A/\mu s$
Reverse recovered energy	E_{rec}					$T_j=25^\circ C$ $T_j=125^\circ C$	0,03 0,12		mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um $\lambda = 1 W/mK$					2,34		K/W

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
		V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_f [A] or I_b [A]	T_j	Min	Typ	Max		

Neutral P. IGBT (T2,T3,T6,T7,T10,T11)

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$				$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	5	5,80	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$			0,002	$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	1,1	1,52 1,70	1,9		V
Collector-emitter cut-off incl diode	I_{CES}		15		$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$			0,0016		mA
Gate-emitter leakage current	I_{GES}		0	600					300	nA
Integrated Gate resistor	R_{gint}							none		Ω
Turn-on delay time	$t_{d(on)}$	$R_{goff}=16 \Omega$ $R_{gon}=16 \Omega$	± 15	350	28	$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	105 105			ns
Rise time	t_r					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	11 16			
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	164 187			
Fall time	t_f					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	74 91			
Turn-on energy loss per pulse	E_{on}					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	0,49 0,66			mWs
Turn-off energy loss per pulse	E_{off}					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	0,76 0,98			
Input capacitance	C_{ies}						1630			
Output capacitance	C_{oss}	$f=1\text{MHz}$	0	25		$T_j=25^\circ\text{C}$	108			pF
Reverse transfer capacitance	C_{rss}						50			
Gate charge	Q_{Gate}					$T_j=25^\circ\text{C}$		167		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						2,56		K/W

Half Bridge FWD (D1,D4,D5,D8,D9,D12)

Diode forward voltage	V_F			15	$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$		2,28 2,39	2,71		V
Reverse leakage current	I_r		1200		$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$			60		μA
Peak reverse recovery current	I_{RRM}	$R_{goff}=16 \Omega$	± 15	350	28	$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	41 44			A
Reverse recovery time	t_{rr}					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	44 110			ns
Reverse recovered charge	Q_{rr}					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	1,47 2,73			μC
Peak rate of fall of recovery current	$d(i_{rec})_{max}/dt$					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	5094 3534			$\text{A}/\mu\text{s}$
Reverse recovery energy	E_{rec}					$T_j=25^\circ\text{C}$ $T_i=125^\circ\text{C}$	0,35 0,71			mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						3,36		K/W

Thermistor

Rated resistance	R				$T_j=25^\circ\text{C}$		21511			Ω
Deviation of R100	$\Delta R/R$	$R100=1486 \Omega$			$T_c=100^\circ\text{C}$	-4,5		+4,5		%
Power dissipation	P				$T_j=25^\circ\text{C}$		210			mW
Power dissipation constant					$T_j=25^\circ\text{C}$		3,5			mW/K
B-value	$B_{(25/50)}$				$T_j=25^\circ\text{C}$		3884			K
B-value	$B_{(25/100)}$				$T_j=25^\circ\text{C}$		3964			K
Vincotech NTC Reference					$T_j=25^\circ\text{C}$			F		

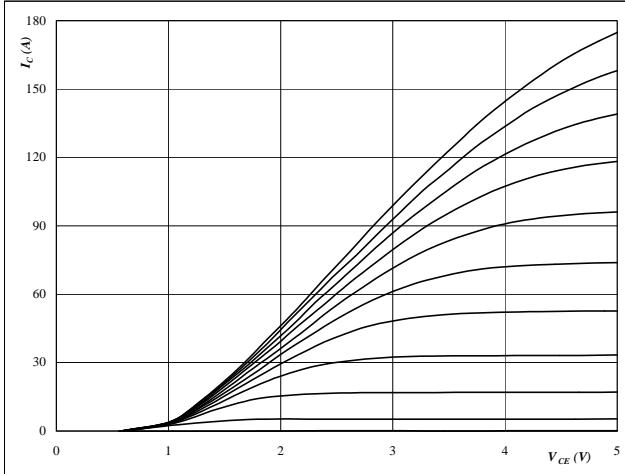
Buck

Half Bridge IGBT and Neutral Point FWD

Figure 1

Typical output characteristics

$$I_C = f(V_{CE})$$



At

$$t_p = 250 \mu s$$

$$T_j = 25 ^\circ C$$

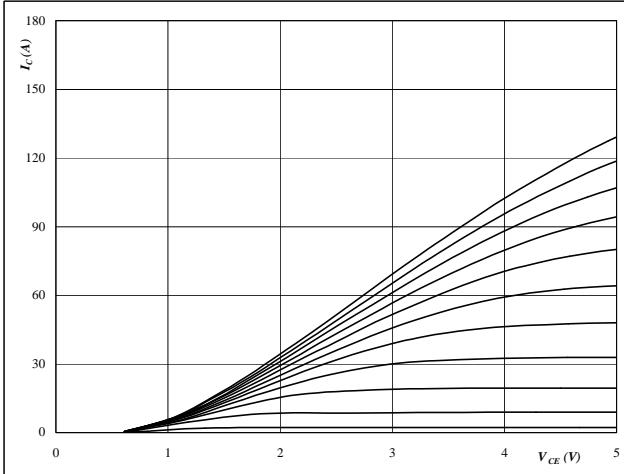
V_{GE} from 7 V to 17 V in steps of 1 V

IGBT

Figure 2

Typical output characteristics

$$I_C = f(V_{CE})$$



At

$$t_p = 250 \mu s$$

$$T_j = 125 ^\circ C$$

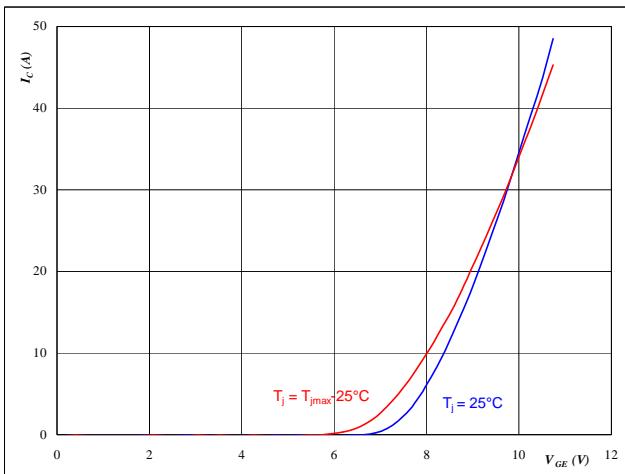
V_{GE} from 7 V to 17 V in steps of 1 V

IGBT

Figure 3

Typical transfer characteristics

$$I_C = f(V_{GE})$$



At

$$t_p = 250 \mu s$$

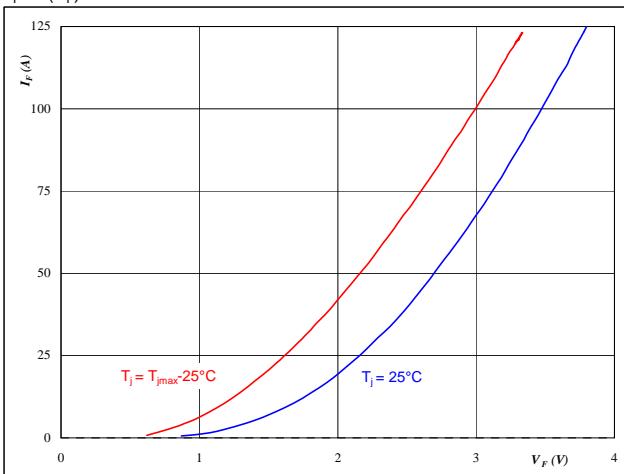
$$V_{CE} = 10 V$$

IGBT

Figure 4

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$



At

$$t_p = 250 \mu s$$

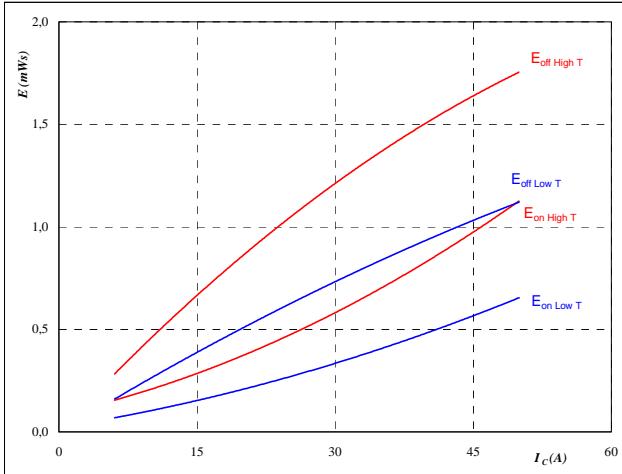
Buck

Half Bridge IGBT and Neutral Point FWD

Figure 5

Typical switching energy losses
as a function of collector current

$$E = f(I_C)$$



With an inductive load at

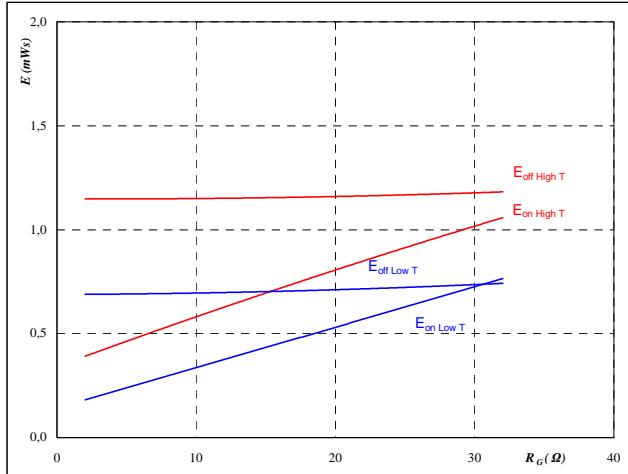
$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ V_{CE} &= 350 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \\ R_{goff} &= 8 \quad \Omega \end{aligned}$$

IGBT

Figure 6

Typical switching energy losses
as a function of gate resistor

$$E = f(R_G)$$



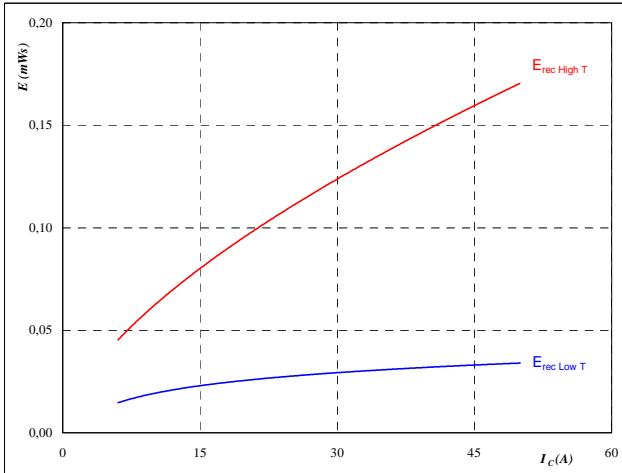
With an inductive load at

$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ V_{CE} &= 350 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 28 \quad \text{A} \end{aligned}$$

Figure 7

Typical reverse recovery energy loss
as a function of collector current

$$E_{rec} = f(I_C)$$



With an inductive load at

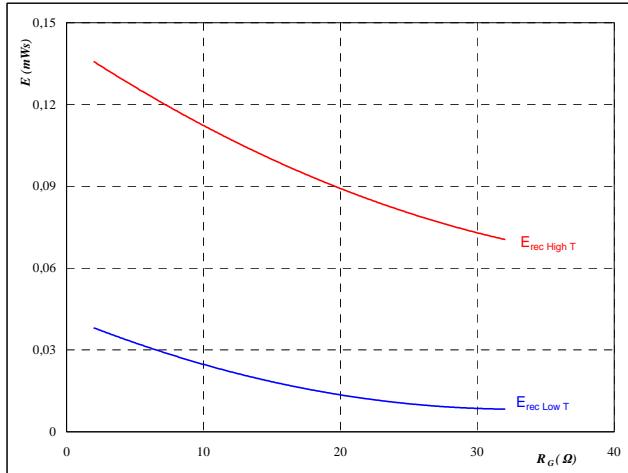
$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ V_{CE} &= 350 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

FWD

Figure 8

Typical reverse recovery energy loss
as a function of gate resistor

$$E_{rec} = f(R_G)$$



With an inductive load at

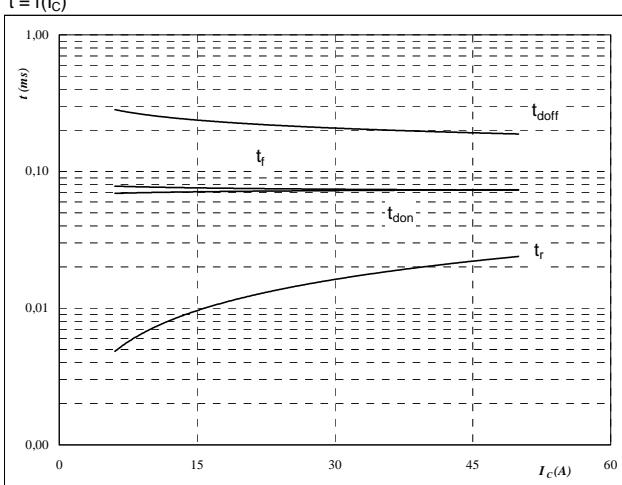
$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ V_{CE} &= 350 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 28 \quad \text{A} \end{aligned}$$

Buck

Half Bridge IGBT and Neutral Point FWD

Figure 9

Typical switching times as a function of collector current
 $t = f(I_C)$



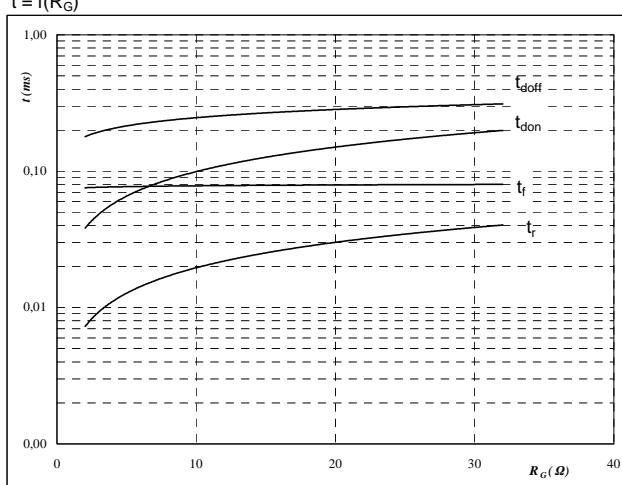
With an inductive load at

$T_j = 125 \text{ } ^\circ\text{C}$
 $V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 8 \text{ } \Omega$
 $R_{goff} = 8 \text{ } \Omega$

IGBT

Figure 10

Typical switching times as a function of gate resistor
 $t = f(R_G)$

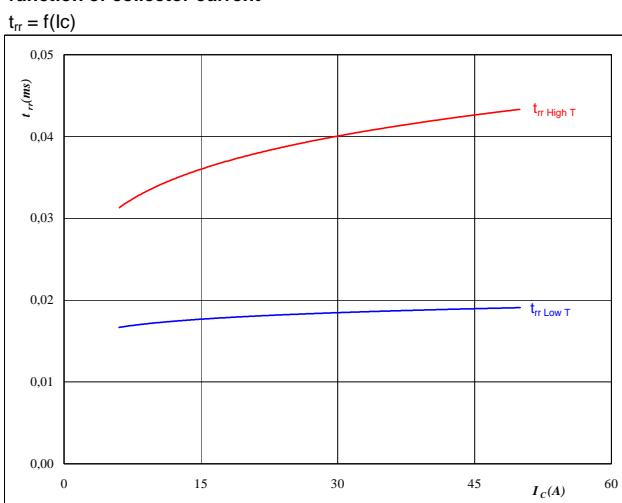


With an inductive load at

$T_j = 125 \text{ } ^\circ\text{C}$
 $V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 28 \text{ A}$

Figure 11

Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_C)$



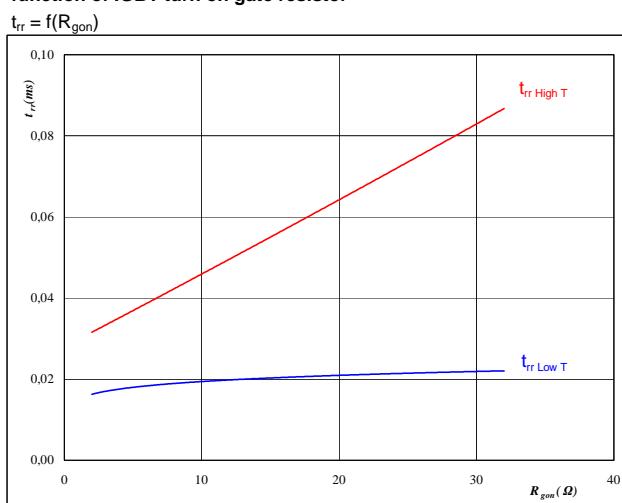
At

$T_j = 25/125 \text{ } ^\circ\text{C}$
 $V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 8 \text{ } \Omega$

FWD

Figure 12

Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$



At

$T_j = 25/125 \text{ } ^\circ\text{C}$
 $V_R = 350 \text{ V}$
 $I_F = 28 \text{ A}$
 $V_{GE} = \pm 15 \text{ V}$

Buck

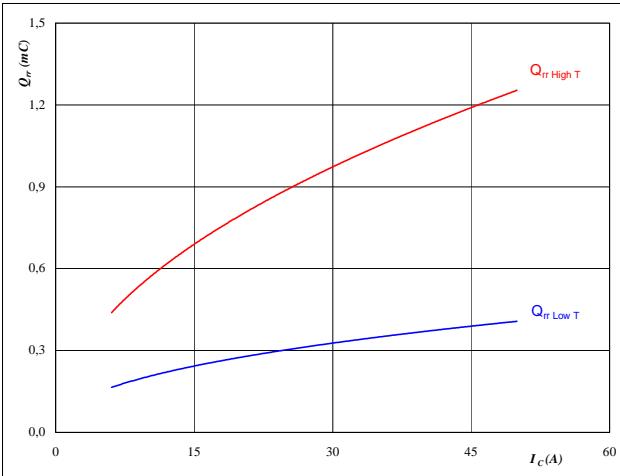
Half Bridge IGBT and Neutral Point FWD

Figure 13

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

FWD



At

$$T_j = 25/125 \quad ^\circ C$$

$$V_{CE} = 350 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

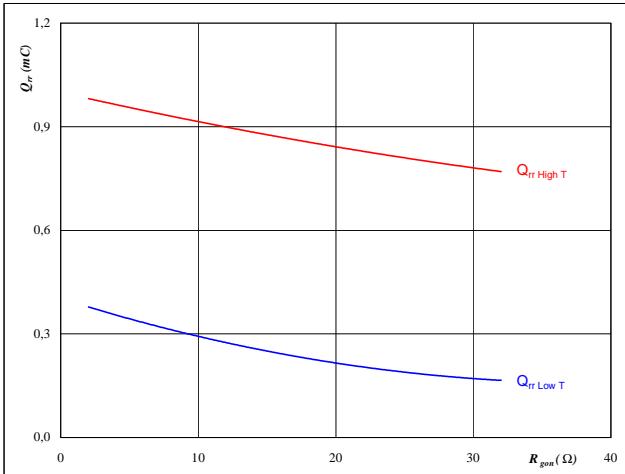
$$R_{gon} = 8 \quad \Omega$$

Figure 14

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$

FWD



At

$$T_j = 25/125 \quad ^\circ C$$

$$V_R = 350 \quad V$$

$$I_F = 28 \quad A$$

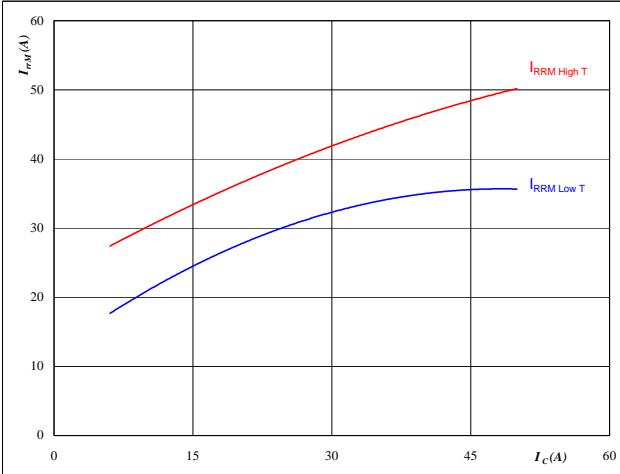
$$V_{GE} = \pm 15 \quad V$$

Figure 15

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

FWD



At

$$T_j = 25/125 \quad ^\circ C$$

$$V_{CE} = 350 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

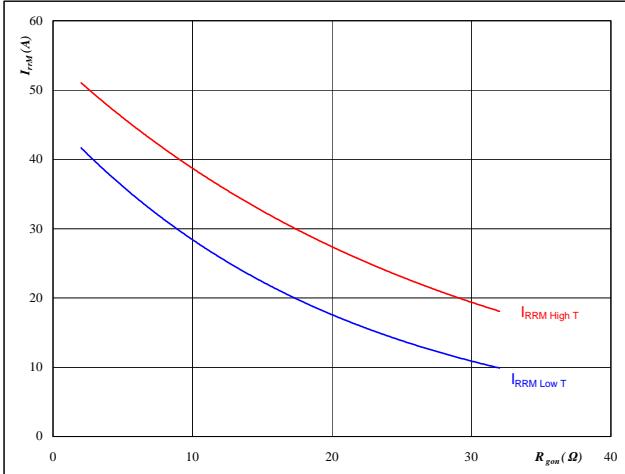
$$R_{gon} = 8 \quad \Omega$$

Figure 16

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$

FWD



At

$$T_j = 25/125 \quad ^\circ C$$

$$V_R = 350 \quad V$$

$$I_F = 28 \quad A$$

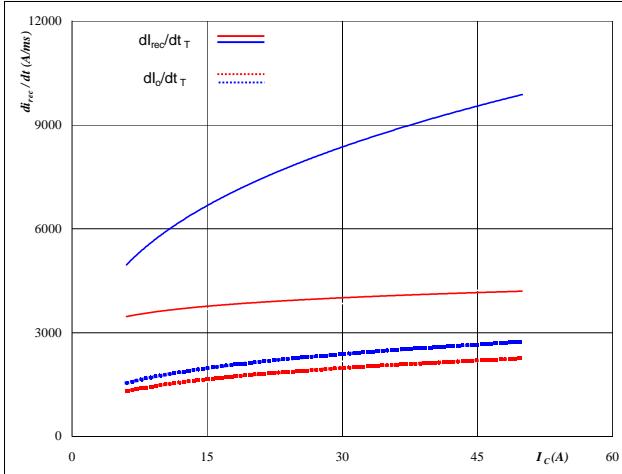
$$V_{GE} = \pm 15 \quad V$$

Buck

Half Bridge IGBT and Neutral Point FWD

Figure 17

Typical rate of fall of forward
and reverse recovery current as a
function of collector current
 $dl_0/dt, dl_{rec}/dt = f(I_c)$



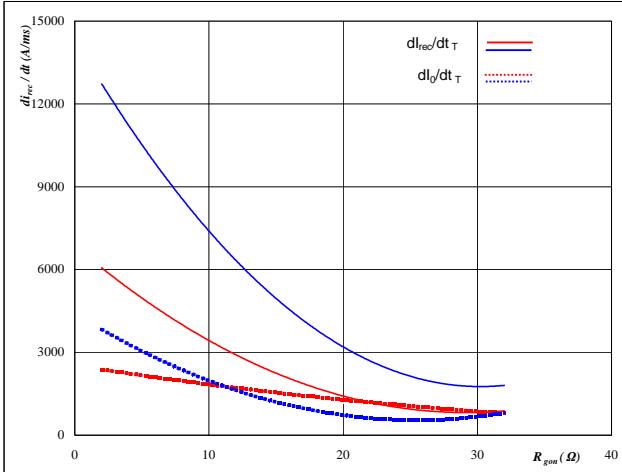
At

T_j = 25/125 °C
 V_{CE} = 350 V
 V_{GE} = ±15 V
 R_{gon} = 8 Ω

FWD

Figure 18

Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor
 $dl_0/dt, dl_{rec}/dt = f(R_{gon})$



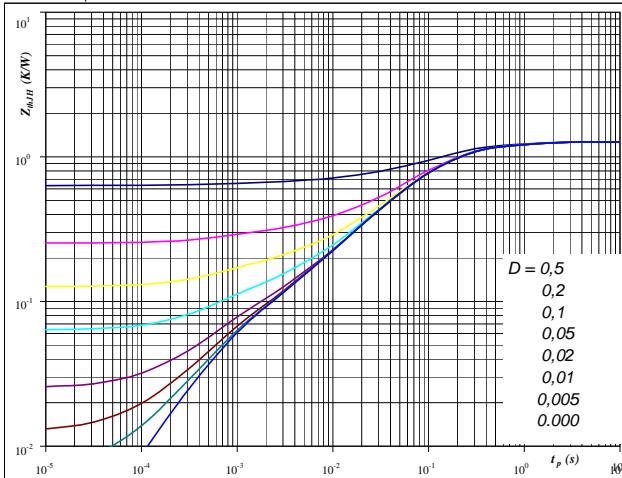
At

T_j = 25/125 °C
 V_R = 350 V
 I_F = 28 A
 V_{GE} = ±15 V

Figure 19

IGBT transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

D = t_p / T
 R_{thJH} = 1,27 K/W

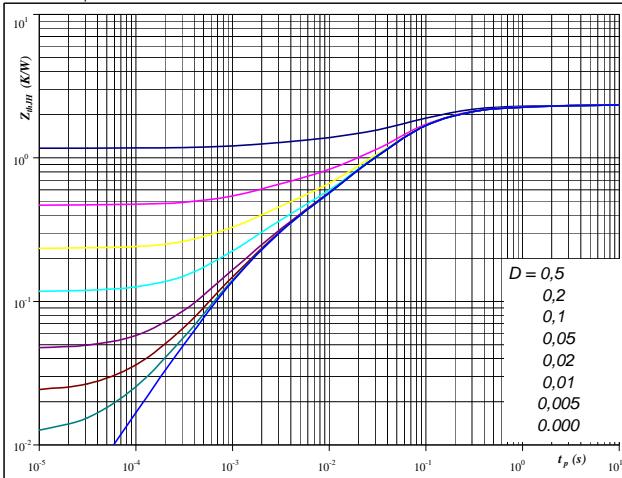
IGBT thermal model values

R (C/W)	Tau (s)
0,18	8,2E-01
0,64	1,3E-01
0,30	4,8E-02
0,10	9,3E-03
0,06	8,0E-04

Figure 20

FWD transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

D = t_p / T
 R_{thJH} = 2,34 K/W

FWD thermal model values

R (C/W)	Tau (s)
0,11	2,4E+00
0,36	3,0E-01
1,41	6,5E-02
0,28	1,1E-02
0,19	1,6E-03

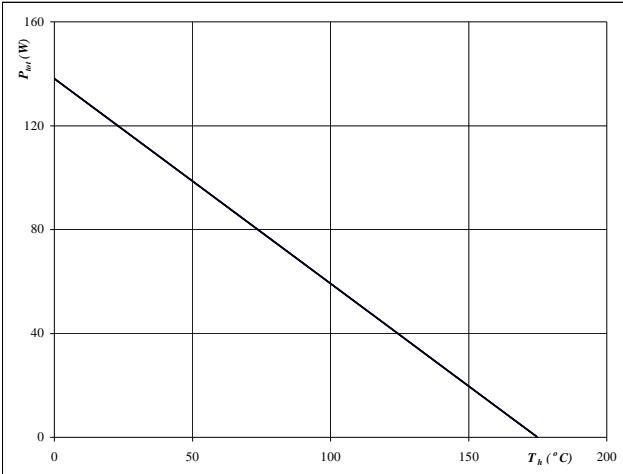
Buck

Half Bridge IGBT and Neutral Point FWD

Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$



At

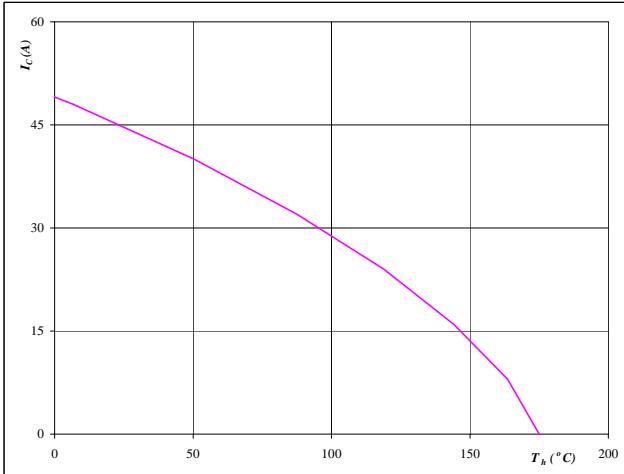
$$T_j = 175 \quad ^\circ\text{C}$$

IGBT

Figure 22

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$



At

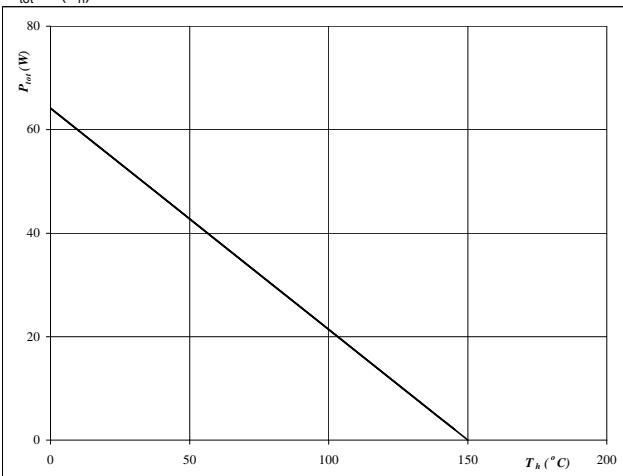
$$T_j = 175 \quad ^\circ\text{C}$$

IGBT

Figure 23

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$



At

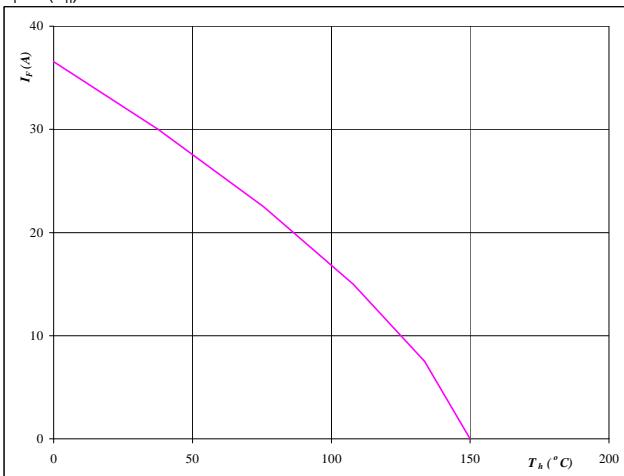
$$T_j = 150 \quad ^\circ\text{C}$$

FWD

Figure 24

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$



At

$$T_j = 150 \quad ^\circ\text{C}$$

FWD

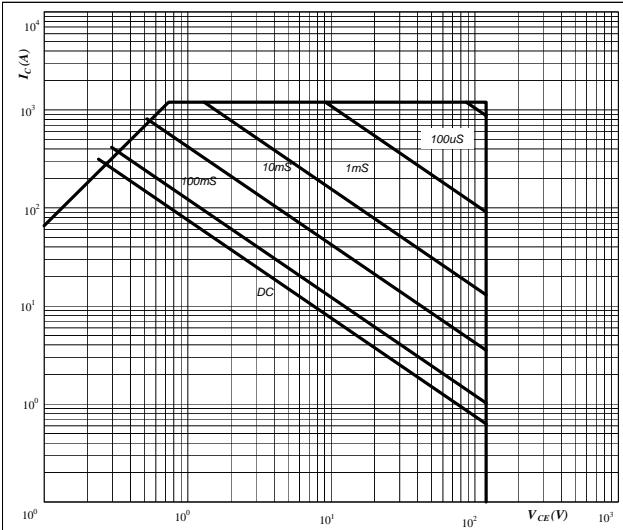
Buck

Half Bridge IGBT and Neutral Point FWD

Figure 25

Safe operating area as a function
of collector-emitter voltage

$$I_C = f(V_{CE})$$



At

D = single pulse

Th = 80 °C

V_{GE} = ±15 V

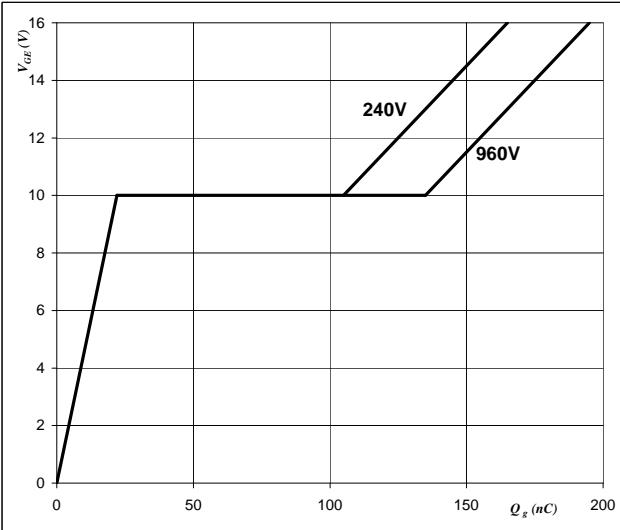
T_j = T_{jmax} °C

IGBT

Figure 26

Gate voltage vs Gate charge

$$V_{GE} = f(Q_g)$$



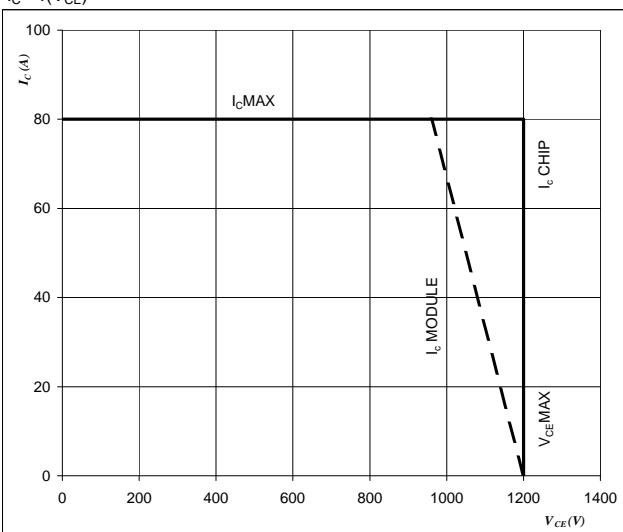
At

I_C = 40 A

Figure 27

Reverse bias safe operating area

$$I_C = f(V_{CE})$$



At

T_j = T_{jmax}-25 °C

DC link minus=DC link plus

Switching mode : 3 level switching

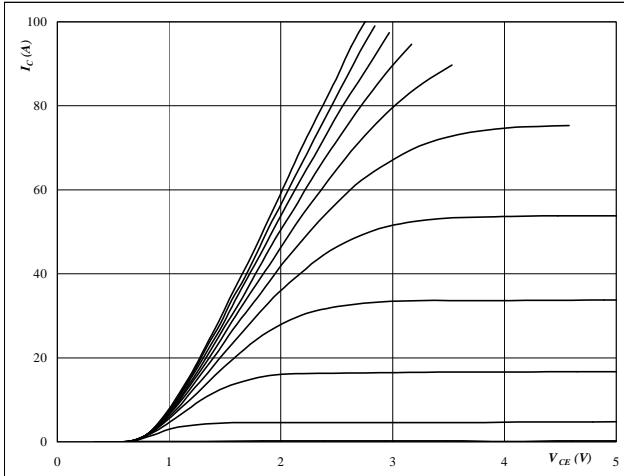
Boost

Neutral Point IGBT and Half Bridge FWD

Figure 1

Typical output characteristics

$$I_C = f(V_{CE})$$



At

$$t_p = 250 \mu\text{s}$$

$$T_j = 25^\circ\text{C}$$

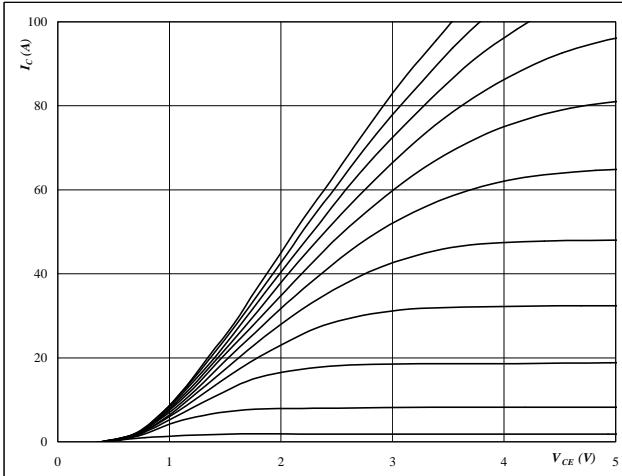
V_{GE} from 7 V to 17 V in steps of 1 V

IGBT

Figure 2

Typical output characteristics

$$I_C = f(V_{CE})$$



At

$$t_p = 250 \mu\text{s}$$

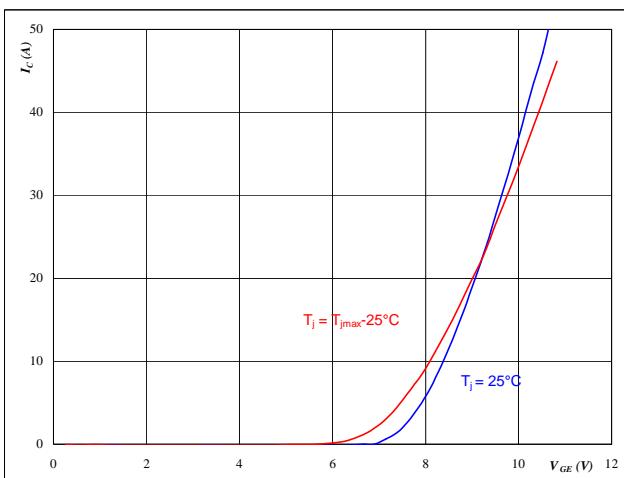
$$T_j = 125^\circ\text{C}$$

V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3

Typical transfer characteristics

$$I_C = f(V_{GE})$$



At

$$t_p = 250 \mu\text{s}$$

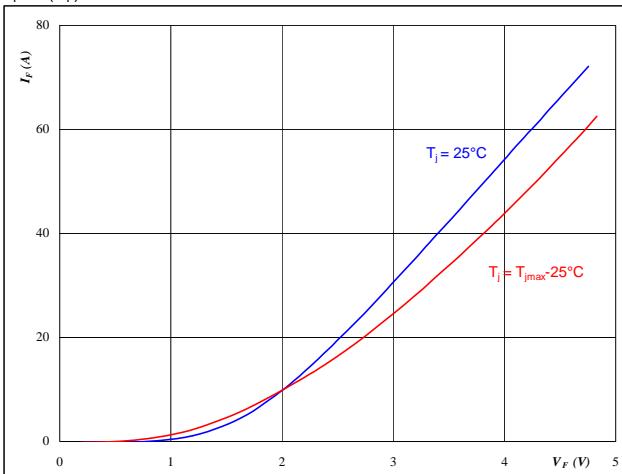
$$V_{CE} = 10 \text{ V}$$

IGBT

Figure 4

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$



At

$$t_p = 250 \mu\text{s}$$

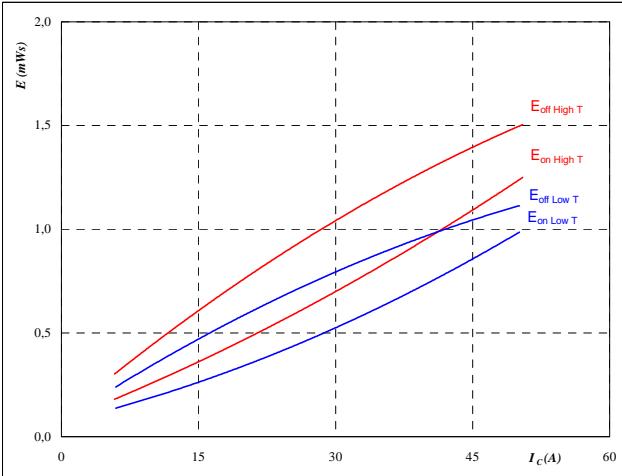
Boost

Neutral Point IGBT and Half Bridge FWD

Figure 5

Typical switching energy losses
as a function of collector current

$$E = f(I_C)$$



With an inductive load at

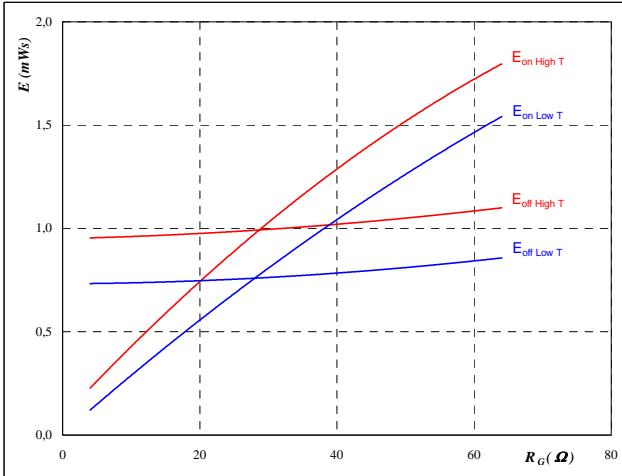
$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ V_{CE} &= 350 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 16 \quad \Omega \\ R_{goff} &= 16 \quad \Omega \end{aligned}$$

IGBT

Figure 6

Typical switching energy losses
as a function of gate resistor

$$E = f(R_G)$$



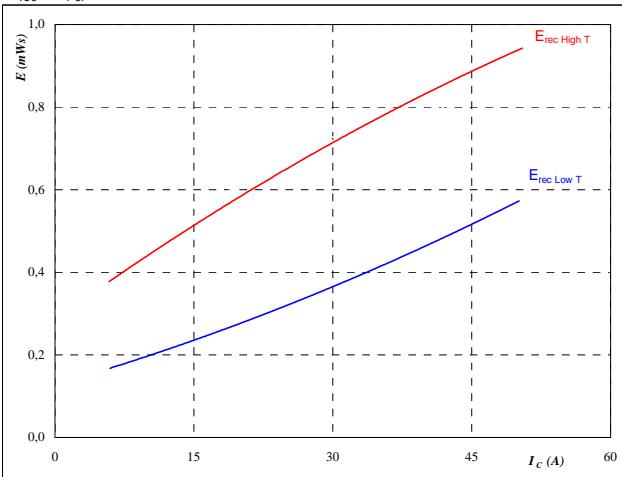
With an inductive load at

$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ V_{CE} &= 350 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 28 \quad \text{A} \end{aligned}$$

Figure 7

Typical reverse recovery energy loss
as a function of collector current

$$E_{rec} = f(I_C)$$



With an inductive load at

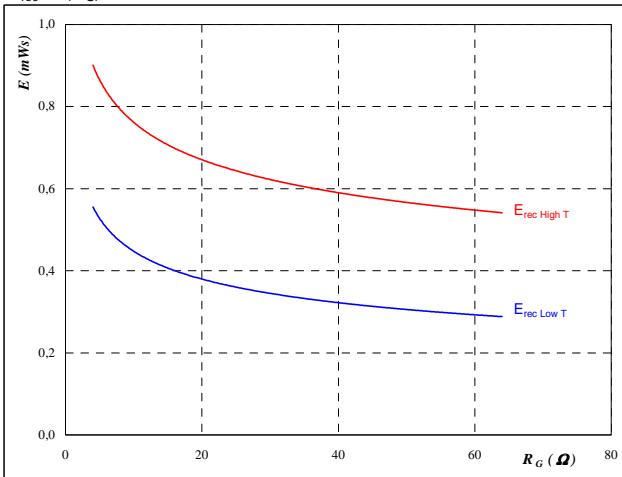
$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ V_{CE} &= 350 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 16 \quad \Omega \end{aligned}$$

FWD

Figure 8

Typical reverse recovery energy loss
as a function of gate resistor

$$E_{rec} = f(R_G)$$



With an inductive load at

$$\begin{aligned} T_j &= 25/125 \quad ^\circ\text{C} \\ V_{CE} &= 350 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 28 \quad \text{A} \end{aligned}$$

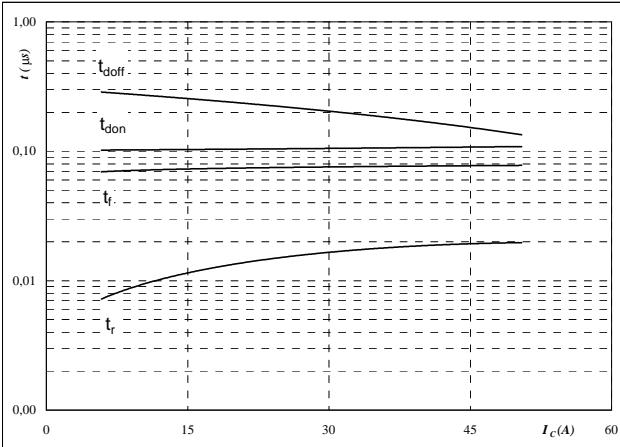
Boost

Neutral Point IGBT and Half Bridge FWD

Figure 9

Typical switching times as a function of collector current

$$t = f(I_C)$$



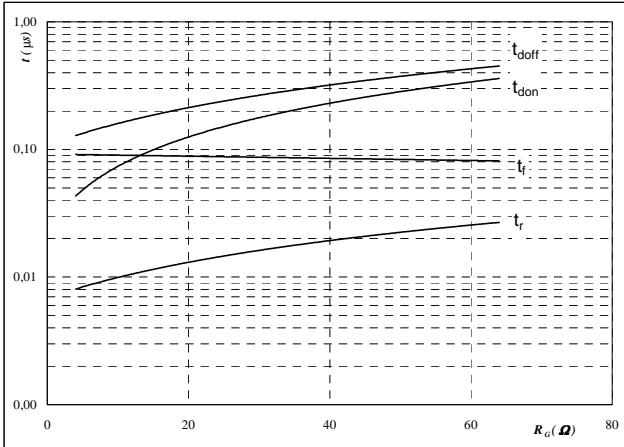
With an inductive load at

T _j =	125	°C
V _{CE} =	350	V
V _{GE} =	±15	V
R _{gon} =	16	Ω
R _{goff} =	16	Ω

Figure 10

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



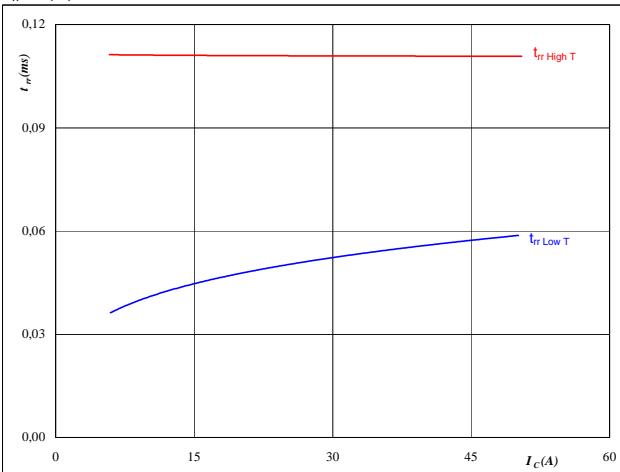
With an inductive load at

T _j =	125	°C
V _{CE} =	350	V
V _{GE} =	±15	V
I _C =	28	A

Figure 11

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



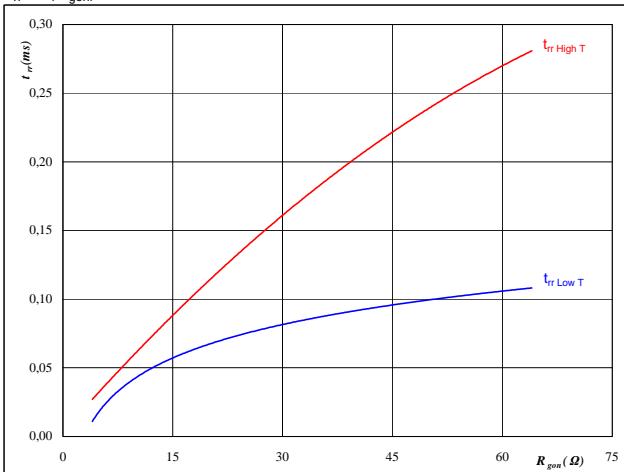
At

T _j =	25/125	°C
V _{CE} =	350	V
V _{GE} =	±15	V
R _{gon} =	16	Ω

Figure 12

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At

T _j =	25/125	°C
V _R =	350	V
I _F =	28	A
V _{GE} =	±15	V

Boost

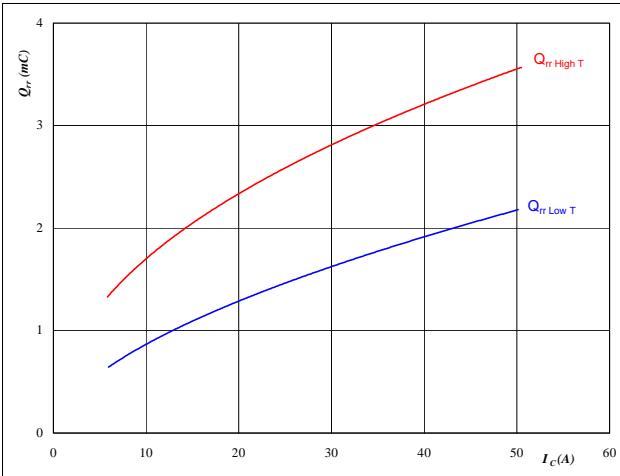
Neutral Point IGBT and Half Bridge FWD

Figure 13

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

FWD



At

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_{CE} = 350 \quad \text{V}$$

$$V_{GE} = \pm 15 \quad \text{V}$$

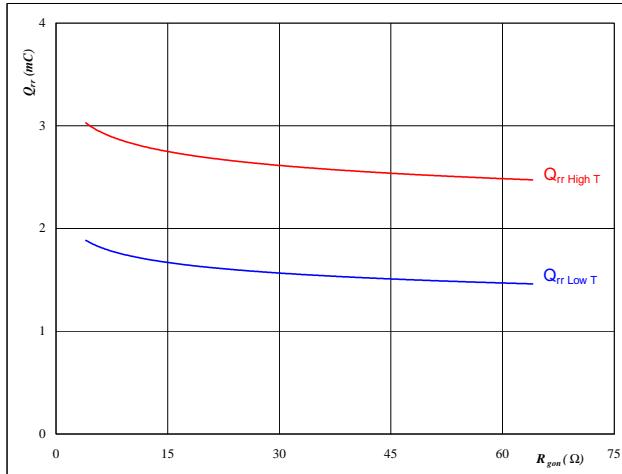
$$R_{gon} = 16 \quad \Omega$$

Figure 14

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$

FWD



At

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_R = 350 \quad \text{V}$$

$$I_F = 28 \quad \text{A}$$

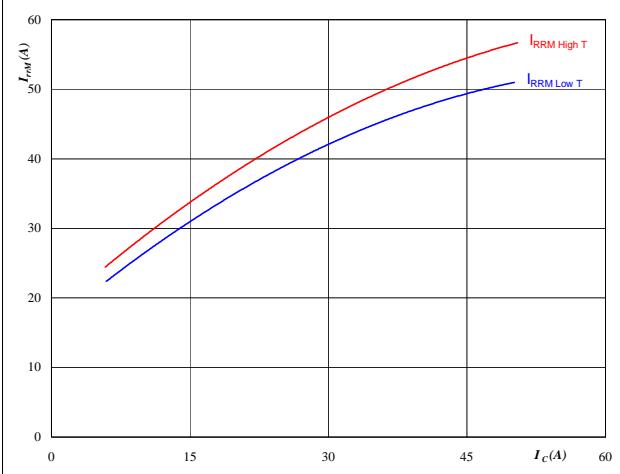
$$V_{GE} = \pm 15 \quad \text{V}$$

Figure 15

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

FWD



At

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_{CE} = 350 \quad \text{V}$$

$$V_{GE} = \pm 15 \quad \text{V}$$

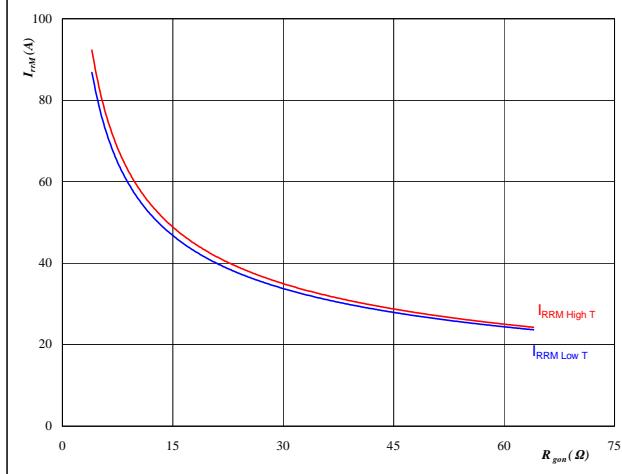
$$R_{gon} = 16 \quad \Omega$$

Figure 16

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$

FWD



At

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_R = 350 \quad \text{V}$$

$$I_F = 28 \quad \text{A}$$

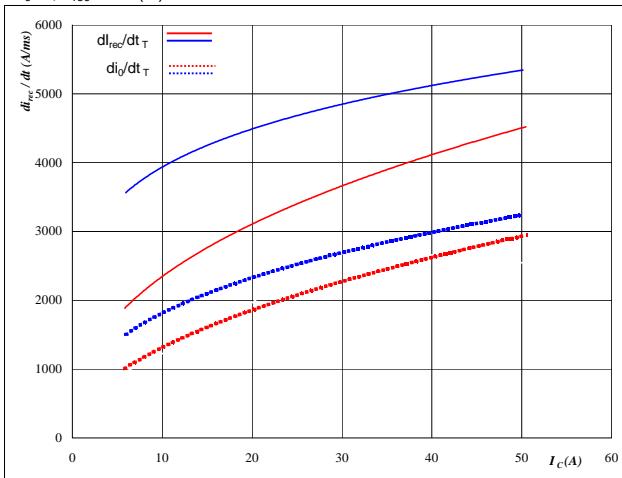
$$V_{GE} = \pm 15 \quad \text{V}$$

Boost

Neutral Point IGBT and Half Bridge FWD

Figure 17

Typical rate of fall of forward
and reverse recovery current as a
function of collector current
 $dl_0/dt, dl_{rec}/dt = f(I_c)$



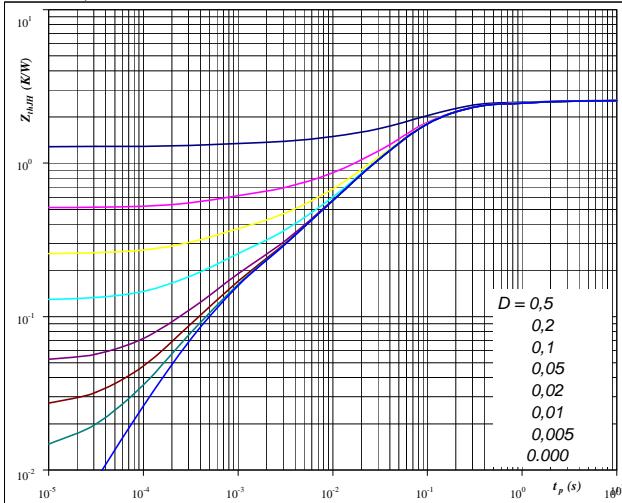
At

T_j = 25/125 °C
 V_{CE} = 350 V
 V_{GE} = ±15 V
 R_{gon} = 16 Ω

Figure 19

IGBT transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

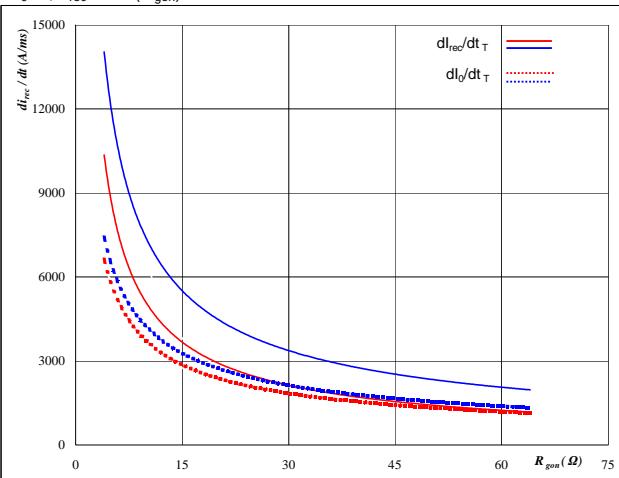
D = t_p / T
 R_{thJH} = 2,56 K/W

IGBT thermal model values

R (C/W)	Tau (s)
0,10	3,0E+00
0,25	4,8E-01
1,64	7,9E-02
0,32	1,9E-02
0,15	4,2E-03
0,11	5,1E-04

Figure 18

Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor
 $dl_0/dt, dl_{rec}/dt = f(R_{gon})$



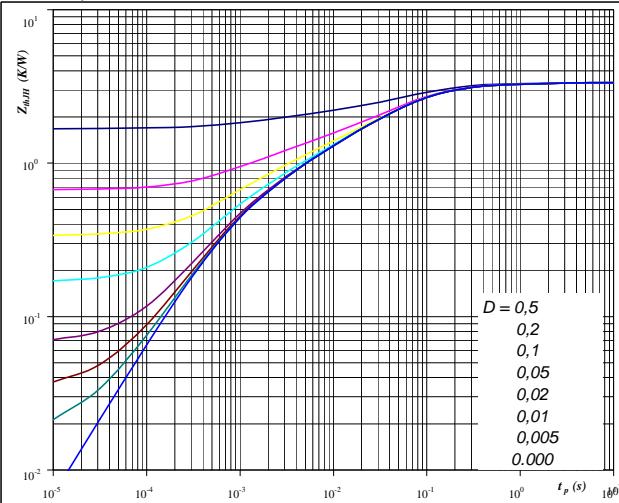
At

T_j = 25/125 °C
 V_R = 350 V
 I_F = 28 A
 V_{GE} = ±15 V

Figure 20

FWD transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

D = t_p / T
 R_{thJH} = 3,36 K/W

FWD thermal model values

R (C/W)	Tau (s)
0,11	2,6E+00
0,25	3,8E-01
1,48	7,2E-02
0,67	1,8E-02
0,50	3,4E-03
0,34	7,0E-04

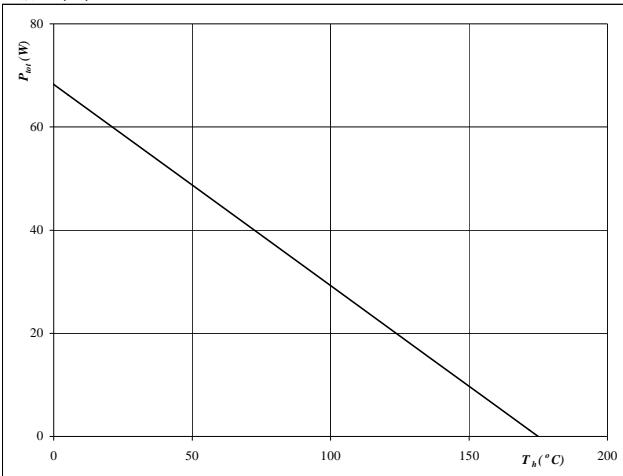
Boost

Neutral Point IGBT and Half Bridge FWD

Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$



At

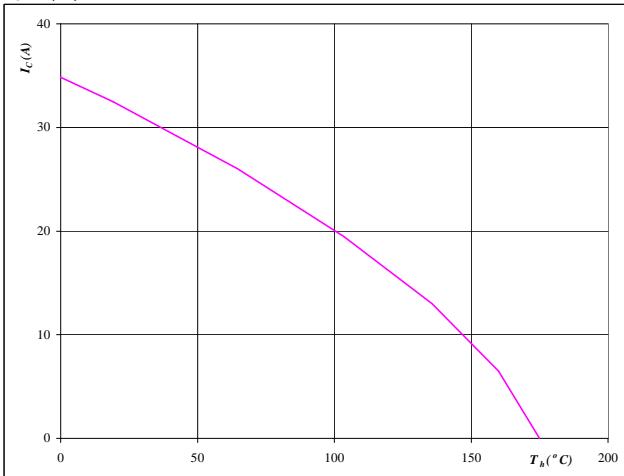
$$T_j = 175 \quad {}^\circ\text{C}$$

IGBT

Figure 22

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$



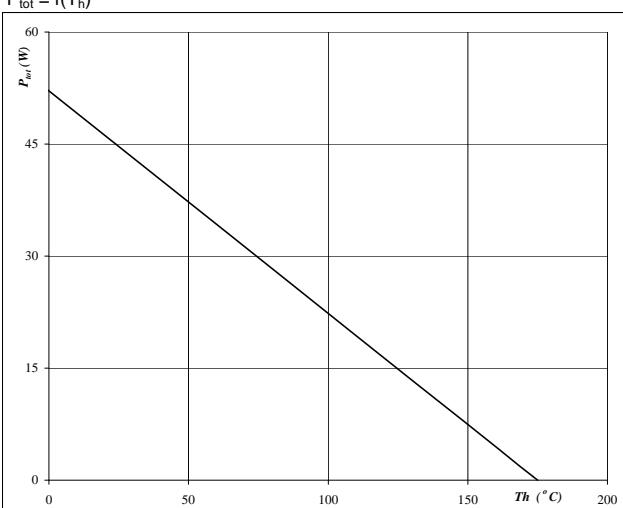
At

$$T_j = 175 \quad {}^\circ\text{C}$$

Figure 23

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$



At

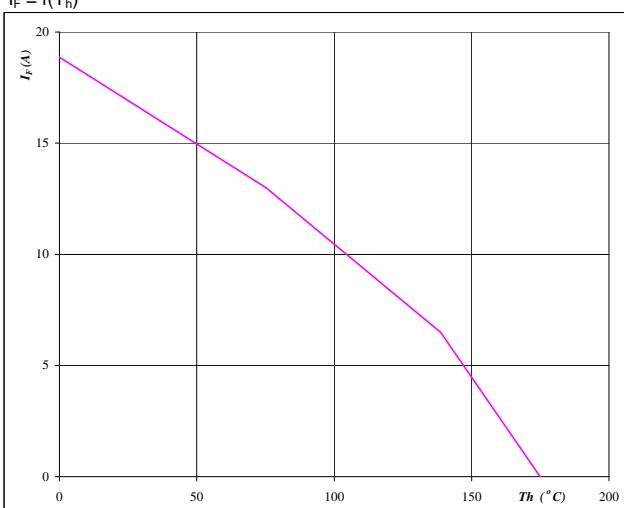
$$T_j = 175 \quad {}^\circ\text{C}$$

FWD

Figure 24

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$



At

$$T_j = 175 \quad {}^\circ\text{C}$$

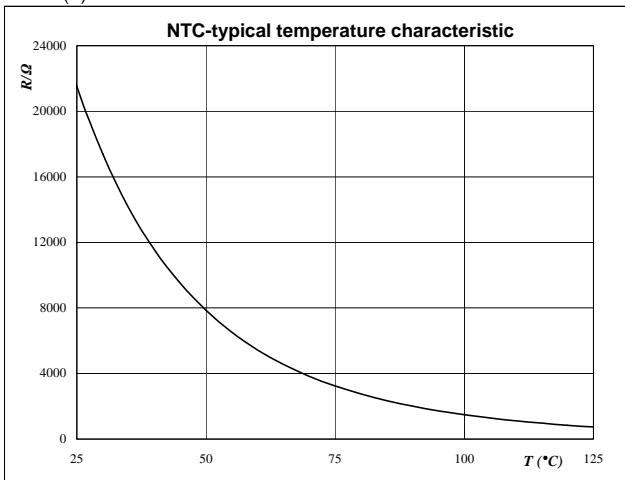
Thermistor

Figure 1

Thermistor

Typical NTC characteristic
as a function of temperature

$$R_T = f(T)$$



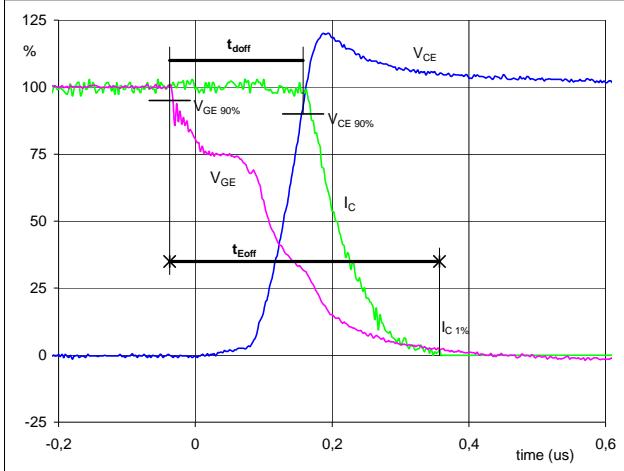
Switching Definitions Neutral Point

General conditions

T_j	=	125 °C
R_{gon}	=	16 Ω
R_{goff}	=	16 Ω

Figure 1

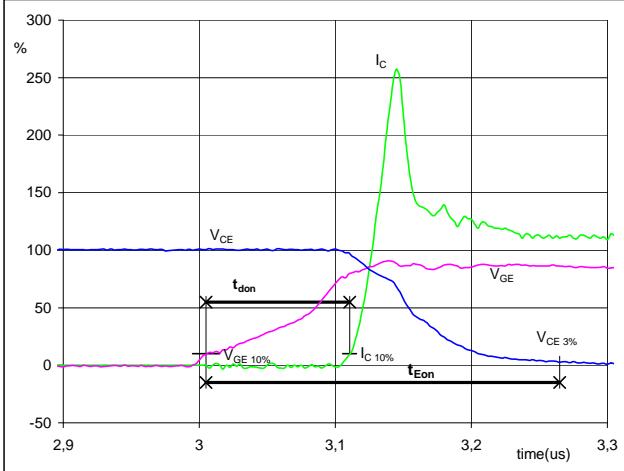
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 $(t_{Eoff} = \text{integrating time for } E_{off})$



$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	350	V
$I_C(100\%) =$	28	A
$t_{doff} =$	0,19	μs
$t_{Eoff} =$	0,39	μs

Figure 2

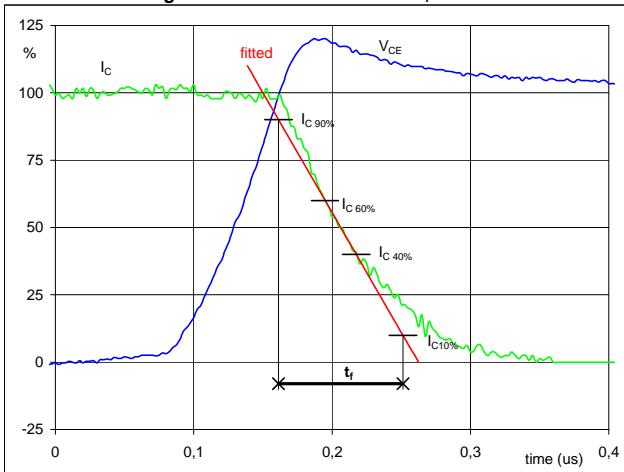
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 $(t_{Eon} = \text{integrating time for } E_{on})$



$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	350	V
$I_C(100\%) =$	28	A
$t_{don} =$	0,11	μs
$t_{Eon} =$	0,26	μs

Figure 3

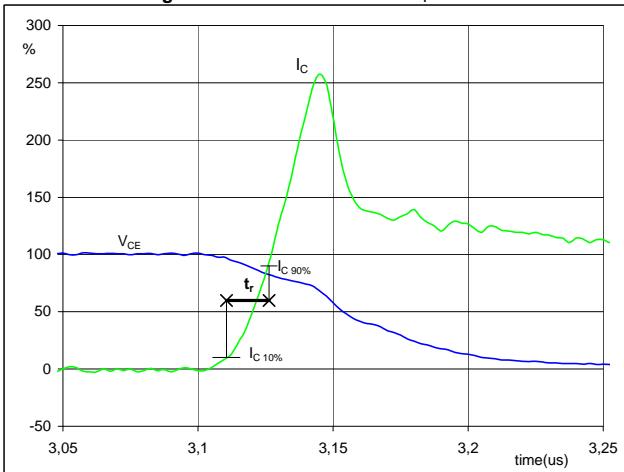
Turn-off Switching Waveforms & definition of t_f



$V_C(100\%) =$	350	V
$I_C(100\%) =$	28	A
$t_f =$	0,09	μs

Figure 4

Turn-on Switching Waveforms & definition of t_r

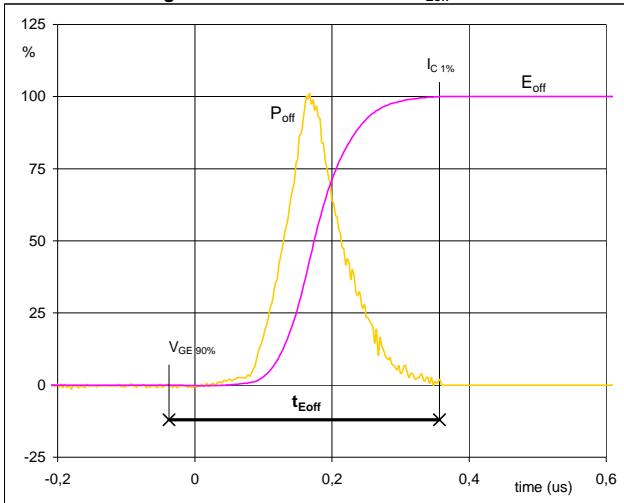


$V_C(100\%) =$	350	V
$I_C(100\%) =$	28	A
$t_r =$	0,02	μs

Switching Definitions Neutral Point

Figure 5

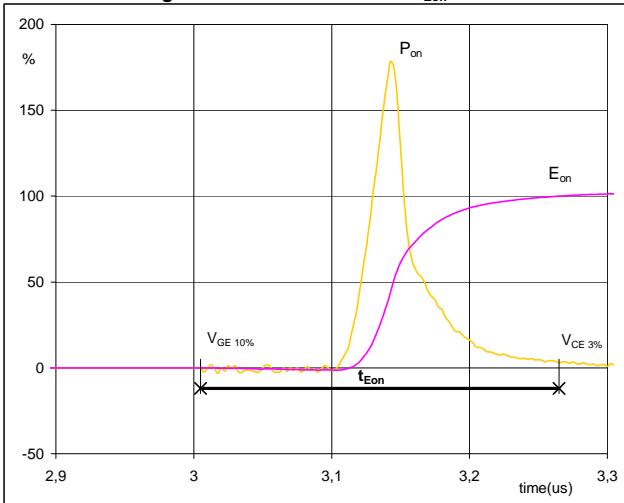
Boost IGBT

Turn-off Switching Waveforms & definition of t_{Eoff}


$P_{off} (100\%) = 9,70 \text{ kW}$
 $E_{off} (100\%) = 0,98 \text{ mJ}$
 $t_{Eoff} = 0,39 \mu\text{s}$

Figure 6

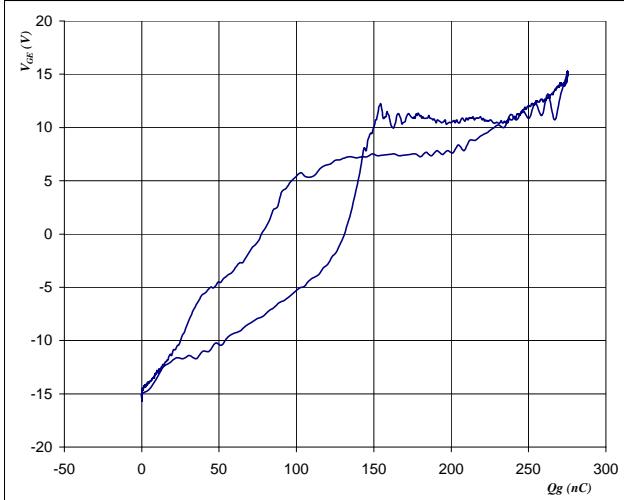
Boost IGBT

Turn-on Switching Waveforms & definition of t_{Eon}


$P_{on} (100\%) = 9,70 \text{ kW}$
 $E_{on} (100\%) = 0,66 \text{ mJ}$
 $t_{Eon} = 0,26 \mu\text{s}$

Figure 7

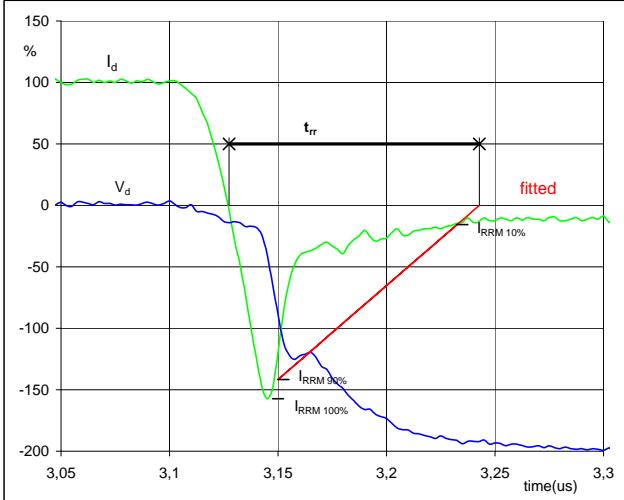
Boost IGBT

Gate voltage vs Gate charge (measured)


$V_{GEoff} = -15 \text{ V}$
 $V_{GEon} = 15 \text{ V}$
 $V_C (100\%) = 350 \text{ V}$
 $I_C (100\%) = 28 \text{ A}$
 $Q_g = 277 \text{ nC}$

Figure 8

Buck FWD

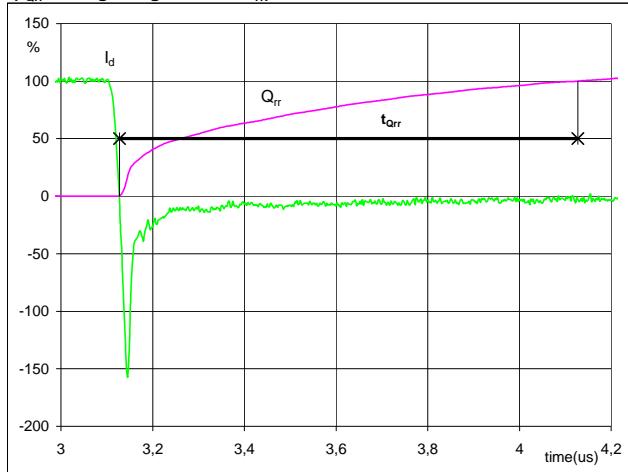
Turn-off Switching Waveforms & definition of t_{rr}


$V_d (100\%) = 350 \text{ V}$
 $I_d (100\%) = 28 \text{ A}$
 $I_{RRM} (100\%) = -44 \text{ A}$
 $t_{rr} = 0,11 \mu\text{s}$

Switching Definitions Neutral Point

Figure 9

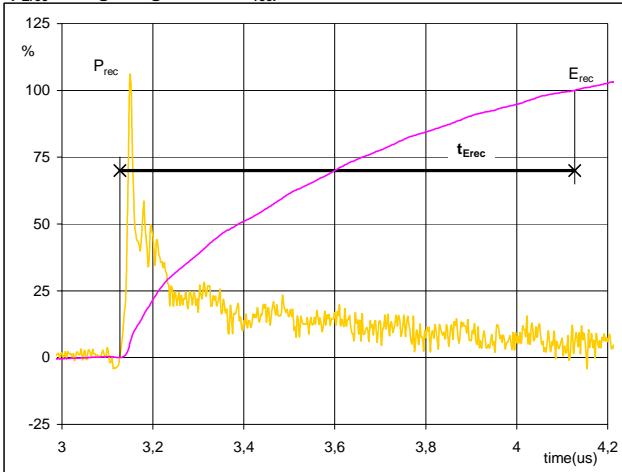
Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$



$I_d(100\%) = 28 \text{ A}$
 $Q_{rr}(100\%) = 2,73 \mu\text{C}$
 $t_{Qrr} = 1,00 \mu\text{s}$

Boost IGBT
Figure 10

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



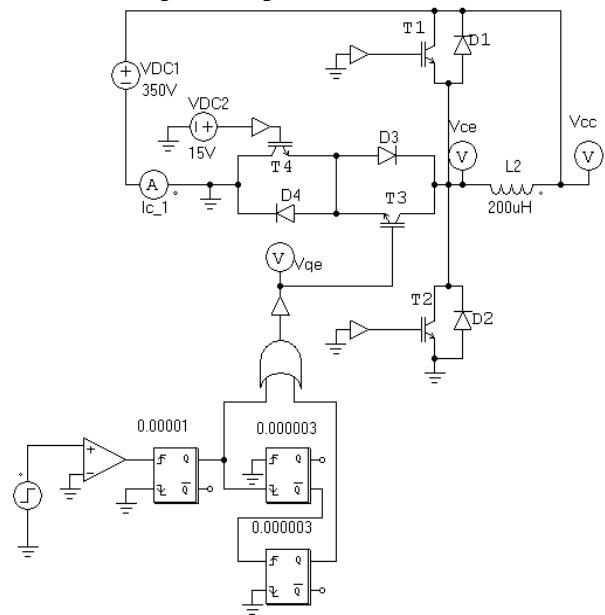
$P_{rec}(100\%) = 9,70 \text{ kW}$
 $E_{rec}(100\%) = 0,71 \text{ mJ}$
 $t_{Erec} = 1,00 \mu\text{s}$

Buck FWD

Measurement circuits

Figure 11

Neutral Point stage switching measurement circuit



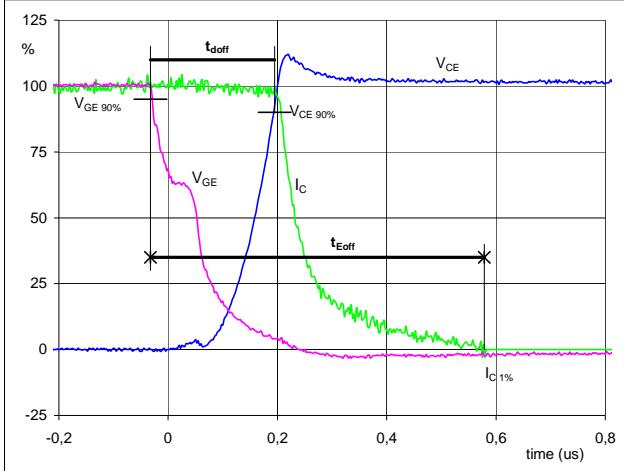
Switching Definitions Half Bridge

General conditions

T_j	=	125 °C
R_{gon}	=	8 Ω
R_{goff}	=	8 Ω

Figure 1

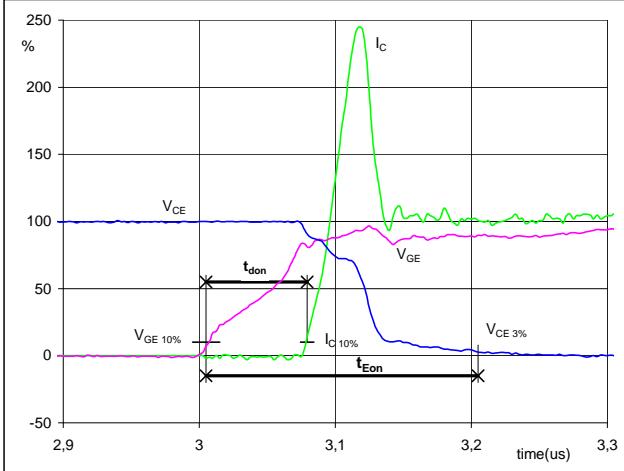
Buck IGBT
 Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 (t_{Eoff} = integrating time for E_{off})



$V_{GE}(0\%) = -15 \text{ V}$
 $V_{GE}(100\%) = 15 \text{ V}$
 $V_C(100\%) = 350 \text{ V}$
 $I_C(100\%) = 28 \text{ A}$
 $t_{doff} = 0,22 \mu\text{s}$
 $t_{Eoff} = 0,61 \mu\text{s}$

Figure 2

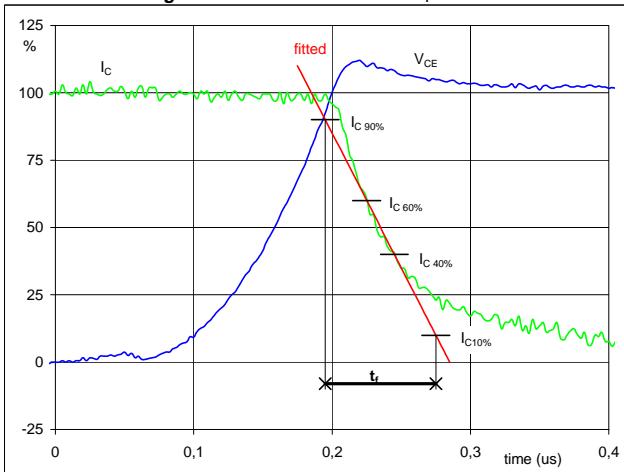
Buck IGBT
 Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 (t_{Eon} = integrating time for E_{on})



$V_{GE}(0\%) = -15 \text{ V}$
 $V_{GE}(100\%) = 15 \text{ V}$
 $V_C(100\%) = 350 \text{ V}$
 $I_C(100\%) = 28 \text{ A}$
 $t_{don} = 0,07 \mu\text{s}$
 $t_{Eon} = 0,20 \mu\text{s}$

Figure 3

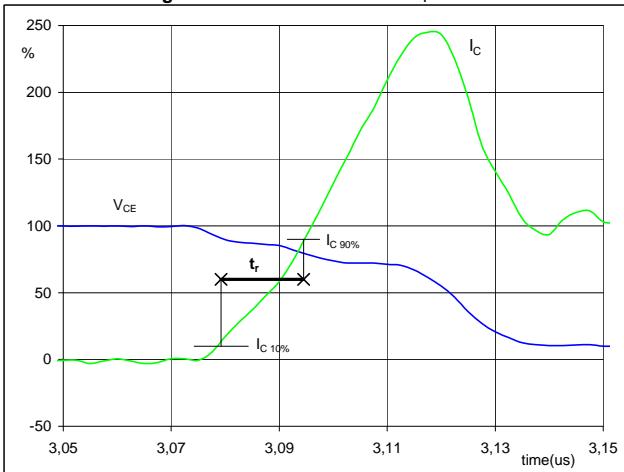
Buck IGBT
 Turn-off Switching Waveforms & definition of t_f



$V_C(100\%) = 350 \text{ V}$
 $I_C(100\%) = 28 \text{ A}$
 $t_f = 0,08 \mu\text{s}$

Figure 4

Buck IGBT
 Turn-on Switching Waveforms & definition of t_r

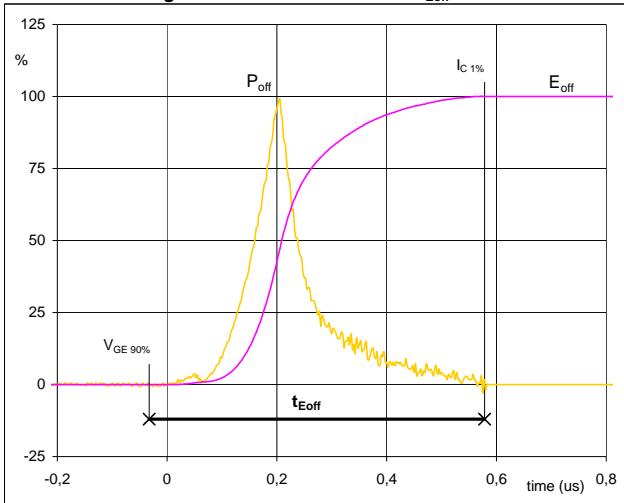


$V_C(100\%) = 350 \text{ V}$
 $I_C(100\%) = 28 \text{ A}$
 $t_r = 0,02 \mu\text{s}$

Switching Definitions Half Bridge

Figure 5

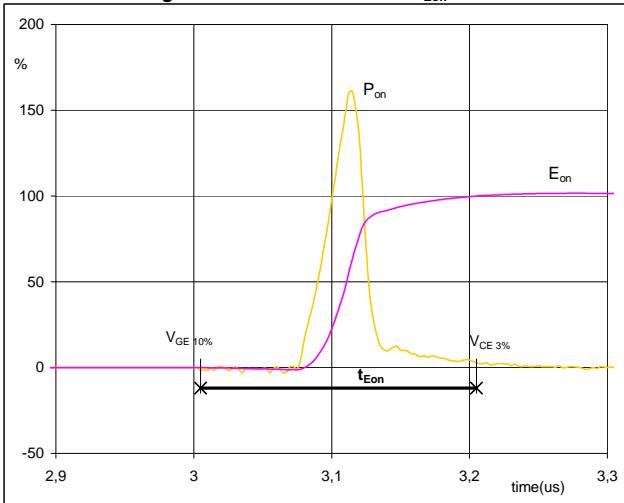
Buck IGBT

Turn-off Switching Waveforms & definition of t_{Eoff}


$P_{off} (100\%) = 9,75 \text{ kW}$
 $E_{off} (100\%) = 1,16 \text{ mJ}$
 $t_{Eoff} = 0,61 \mu\text{s}$

Figure 6

Buck IGBT

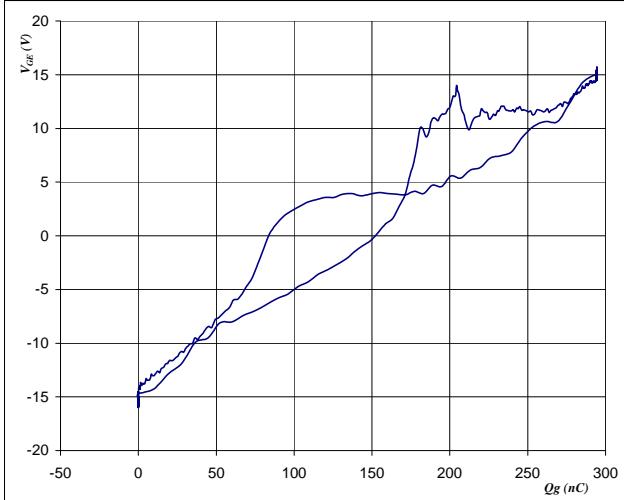
Turn-on Switching Waveforms & definition of t_{Eon}


$P_{on} (100\%) = 9,75 \text{ kW}$
 $E_{on} (100\%) = 0,52 \text{ mJ}$
 $t_{Eon} = 0,20 \mu\text{s}$

Figure 7

Buck IGBT

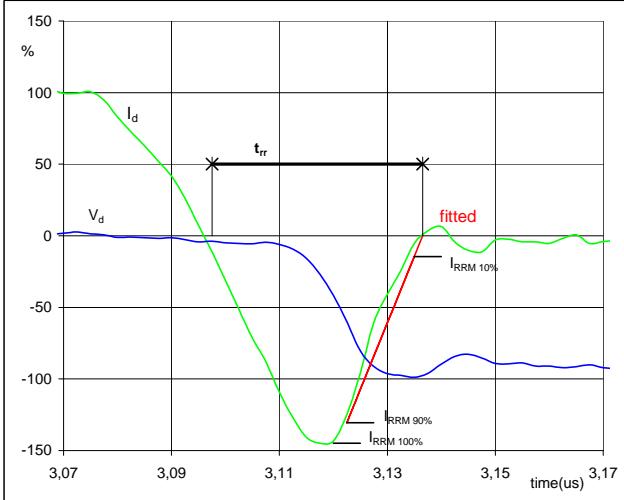
Gate voltage vs Gate charge (measured)



$V_{GEoff} = -15 \text{ V}$
 $V_{GEon} = 15 \text{ V}$
 $V_C (100\%) = 350 \text{ V}$
 $I_C (100\%) = 28 \text{ A}$
 $Q_g = 299,41 \text{ nC}$

Figure 8

Boost FWD

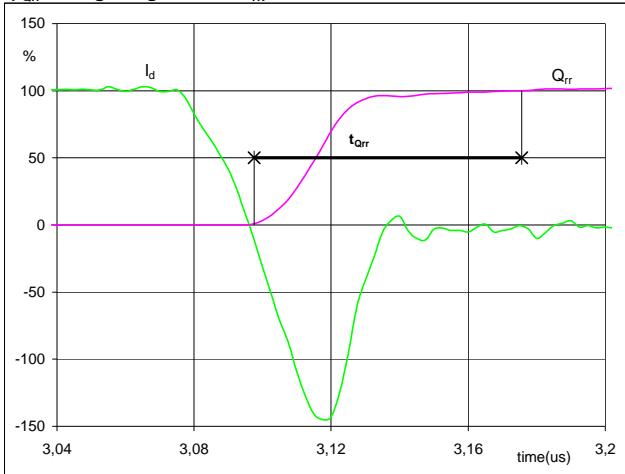
Turn-off Switching Waveforms & definition of t_{rr}


$V_d (100\%) = 350 \text{ V}$
 $I_d (100\%) = 28 \text{ A}$
 $I_{RRM} (100\%) = -41 \text{ A}$
 $t_{rr} = 0,04 \mu\text{s}$

Switching Definitions Half Bridge

Figure 9

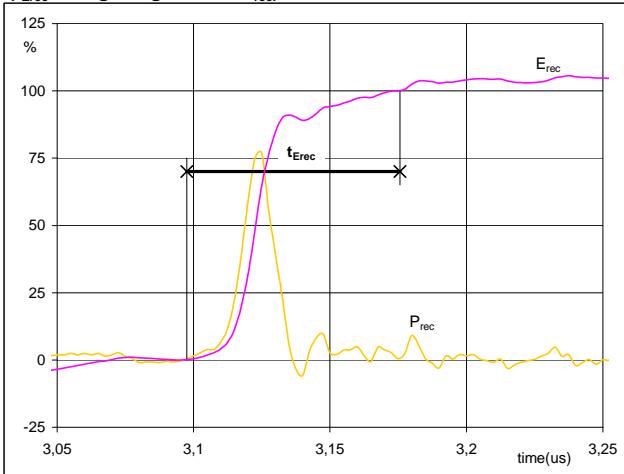
Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$



$$\begin{aligned} I_d(100\%) &= 28 \quad \text{A} \\ Q_{rr}(100\%) &= 0,92 \quad \mu\text{C} \\ t_{Qrr} &= 0,08 \quad \mu\text{s} \end{aligned}$$

Figure 10

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$

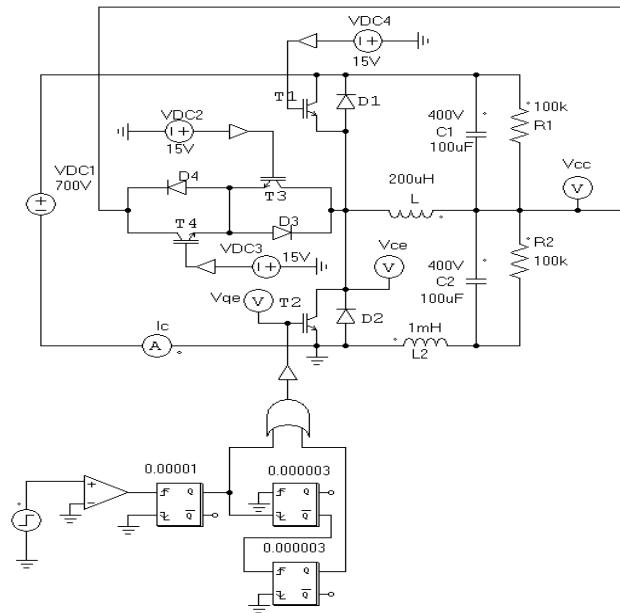


$$\begin{aligned} P_{rec}(100\%) &= 9,75 \quad \text{kW} \\ E_{rec}(100\%) &= 0,12 \quad \text{mJ} \\ t_{Erec} &= 0,08 \quad \mu\text{s} \end{aligned}$$

Measurement circuits

Figure 11

Half Bridge stage switching measurement circuit



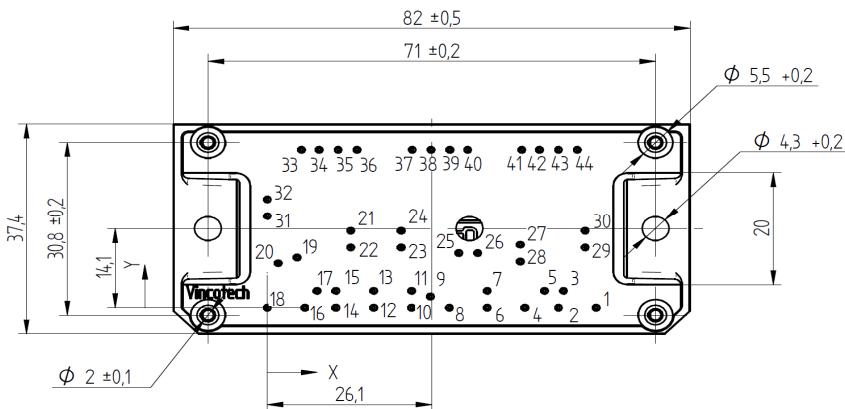
Ordering Code and Marking - Outline - Pinout

Ordering Code & Marking

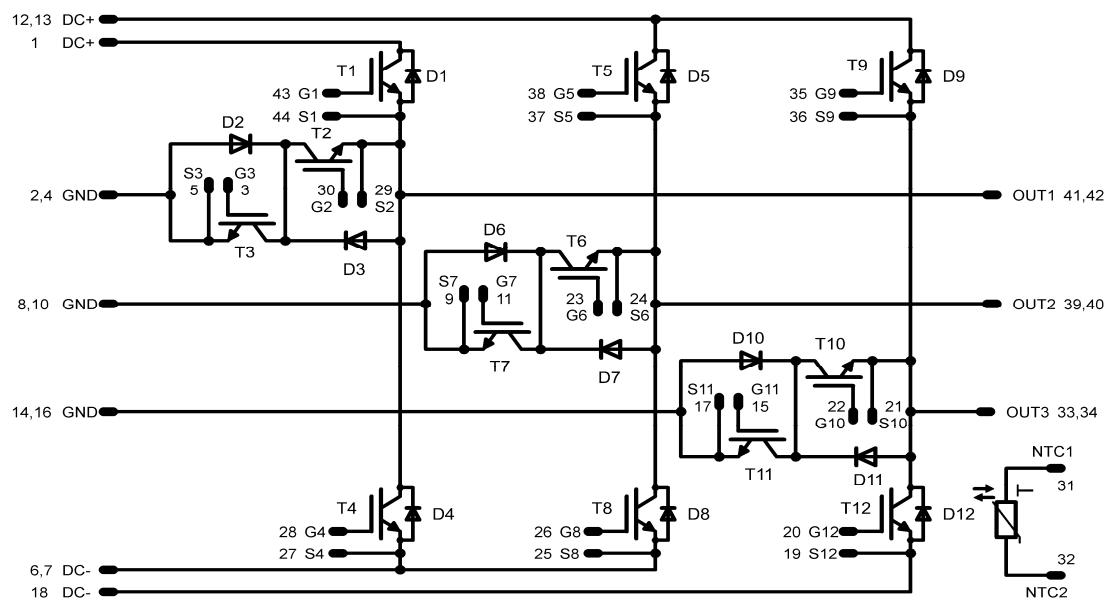
Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste 12mm housing	10-FY12M3A040SH-M749F08	M749F08	M749F08
without thermal paste 17mm housing	10-F112M3A040SH-M749F09	M749F09	M749F09

Outline

Pin	X	Y	Pin	X	Y
1	52,2	0	23	21,25	10,7
2	46,2	0	24	21,25	13,7
3	47	3	25	30,4	9,7
4	40,9	0	26	33,4	9,7
5	44	3	27	40,15	11,2
6	34,9	0	28	40,15	8,2
7	34,9	3	29	50,45	10,7
8	28,9	0	30	50,45	13,7
9	25,9	2	31	0	16,35
10	22,9	0	32	0	19,35
11	22,9	3	33	5,45	28,2
12	16,9	0	34	8,25	28,2
13	16,9	3	35	11,25	28,2
14	10,9	0	36	14,25	28,2
15	10,9	3	37	23	28,2
16	6	0	38	26	28,2
17	7,9	3	39	29	28,2
18	0	0	40	31,8	28,2
19	4,75	8,9	41	40,4	28,2
20	1,75	7,9	42	43,2	28,2
21	13,25	13,7	43	46,2	28,2
22	13,25	10,7	44	49,2	28,2



Pinout



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